Call for Papers
Track 2 - Industrial Communication Technologies and Systems

Track chairs
Svetlana Girs §, Federico Tramarin◆

§ Mälardalen University, Sweden, svetlana.girs@mdh.se
◆ University of Modena and Reggio Emilia, Italy, federico.tramarin@unimore.it

❖ **FOCUS**. The track is focused on industrial communication technologies, including modeling, analysis, and synthesis of provably correct systems, as well as characterization of application requirements, implementations, performance evaluation, validation, and case studies.

❖ **TOPICS**
- ❖ Industrial networks
- ❖ IP-based and web-based industrial communications
- ❖ Integration and interoperability of automation networks
- ❖ Middleware for industrial communications and decentralized control
- ❖ Software-Defined Networks and cognitive radio networks
- ❖ Wireless instrumentation and wireless sensor networks
- ❖ Mesh, relay, and multi-hop industrial networks
- ❖ Wireless coexistence, spectrum-sharing and radio resource management in industrial environments
- ❖ Dependability, fault tolerance, and safety in industrial networks
- ❖ Information security in industrial communications
- ❖ Industrial Internet of Things (IIoT)
- ❖ Machine-to-machine (M2M) communications
- ❖ Communication technologies for Industry 4.0
- ❖ Remote configuration and network management
- ❖ Real-time communication and precise synchronization
- ❖ Event-driven and time-triggered communications
- ❖ Quality of Service (QoS) and performance indexes
- ❖ Time-Sensitive Networking

❖ **AIM.** The aim of the conference is to bring together researchers and practitioners from the industry and academia and provide them with a platform to report on recent advances and developments in the newly emerging areas of technology, as well as actual and potential applications to industrial and factory automation.

❖ **CONFERENCE FORMAT**. The conference will comprise multi-track sessions for regular papers, to present significant and novel research results with a prospect for a tangible impact on the research area and potential implementations, as well as work-in-progress (WiP) and industry practice sessions.

❖ **AUTHOR'S SCHEDULE (2022)**

❖Regular and special sessions papers

Submission deadline	April 1
Acceptance notification	May 6
Deadline for final manuscripts	June 17

❖Work-in-progress/Industry practice papers

Submission deadline	May 13
Acceptance notification	June 10
Deadline for final manuscripts	June 17

Track Program Committee

- ❖ Dave Cavalcanti, Intel Labs, Oregon, USA
- ❖ Gianluca Cena, CNR-IEIIT, Torino, Italy
- ❖ Manuel Cheminod, CNR-IEIIT, Torino, Italy
- ❖ Ramez Daoud, The American University in Cairo, Egypt
- ❖ Ognjen Dobrijevic, ABB Corporate Research, Västerås, Sweden
- ❖ Tommaso Fedullo, University of Modena and Reggio Emilia / University of Padova, Italy
- ❖ Paolo Ferrari, University of Brescia, Italy
- ❖ Joaquim Ferreira, University of Aveiro, Aveiro, Portugal
- ❖ Marisol García Valls, Universitat Politècnica de València, Spain
- ❖ Hans Hansson, Mälardalen University, Sweden
- ❖ Ricardo J. Rodríguez, Universidad de Zaragoza, Zaragoza, Spain
- ❖ Juergen Jasperneite, Fraunhofer IOSB-INA, Germany
- ❖ Kristina Kunert, Umeå University, Umeå, Sweden
- ❖ Luca Leonardi, University of Catania, Catania, Italy
- ❖ Luis Lino Ferreira, School of Engineering of the Politechnic Institute of Porto, Porto, Portugal
- ❖ Michele Luvisotto, Hitachi ABB Power Grids,
- ❖ Aamir Mahmood, Mid Sweden University, Sundsvall, Sweden
- ❖ Alberto Morato, CNR-IEIIT, Padova, Italy
- ❖ Roman Obermaisser, University of Siegen, Germany
- ❖ Federico Perez, University of the Basque Country UPV/EHU, Bilbao, Spain
- ❖ Paulo Portugal, University of Porto, Portugal
- ❖ Julián Proenza Arenas, University of the Balearic Islands, Palma, Spain
- ❖ Sasikumar Punnekkat, Mälardalen University, Sweden
- ❖ Markus Rentschler, Balluff GmbH, Germany
- ❖ Stefano Scanzio, CNR-IEIIT, Torino, Italy
- ❖ Oscar Seijo, Ikerlan Technology Research Centre, Mondragón, Spain
- ❖ Michael Short, Teesside University, Middlesbrough, UK
- ❖ Emiliano Sisinni, University of Brescia, Italy
- ❖ Susruth Sudhakaran, Intel Labs, Oregon, USA
- ❖ Abhilash Thekkilakkattil, Ericsson AB, Stockholm, Sweden
- ❖ Eduardo Tovar, CISTER-ISEP, Porto, Portugal
- ❖ Henning Trsek, Institute Industrial IT - inIT / TH-OWL, Germany
- ❖ Iñaki Val, Ikerlan Technology Research Centre, Mondragón, Spain
- ❖ Francisco Vasques, University of Porto, Portugal
- ❖ Andreas Willig, University of Canterbury, New Zealand
- ❖ Lukasz Wisniewski, Institute industrial IT – inIT / TH-OWL, Germany
- ❖ Claudio Zunino, CNR-IEIIT, Torino, Italy